

Standards Manager Web Standards List
VITA-VMEbus International Trade Association

| Id | Number | Title | Year | Organization | Page |
|-----------|---------------|---|-------------|---------------------|-------------|
| 1 | 42.0 | XMC | 2021 | VITA | |
| 2 | 65.0 | OpenVPX System Standard | 2021 | VITA | |
| 3 | 65.1 | This standard documents variations of Slot, Backplane, and Modules Profiles. As part of the Slot Profile Description, there are also some Connector Modules defined. This document is primarily tables which are referenced by [VITA 65.0]. | 2021 | VITA | |
| 4 | 68.2 | VPX Standard S-Parameter Definition | 2021 | VITA | |
| 5 | 76.0 | High Performance Cable Standard - Ruggedized 10 Gbaud Bulkhead Connector for Cu and AOC Cables | 2021 | VITA | |
| 6 | 46.31 | Higher Data Rate VPX, Solder Tail | 2020 | VITA | |
| 7 | 46.31 | Higher Data Rate VPX, Solder Tail | 2020 | VITA | |
| 8 | 67.2 | Coaxial Interconnect on VPX, 8 Position SMPM Configuration - The objective of this standard is to detail the configuration and interconnect within the structure of VITA 67.0 enabling a VITA 46 interface containing multi-position blind mate analog connecto | 2020 | VITA | |
| 9 | 67.2 | Coaxial Interconnect on VPX, 8 Position SMPM Configuration - The objective of this standard is to detail the configuration and interconnect within the structure of VITA 67.0 enabling a VITA 46 interface containing multi-position blind mate analog connecto | 2020 | VITA | |
| 10 | 67.3 | Coaxial Interconnect on VPX, Spring-Loaded Contact on Backplane | 2020 | VITA | |
| 11 | 67.3 | Coaxial Interconnect on VPX, Spring-Loaded Contact on Backplane | 2020 | VITA | |
| 12 | 62.2 | This standard provides requirements for building a 270 volt/3U or 6U class power supply module that can be used to power a VPX chassis in the VITA 62 family of standards in high altitude applications. | 2020 | VITA | |
| 13 | 40 | Service and Status Indicator Standard. This standard defines the colors, behaviors, placement, and labeling of service indicator lamps for boards, field replaceable units, and enclosures. | 2020 | VITA | |
| 14 | 46.30 | Higher Data Rate VPX | 2020 | VITA | |
| 15 | 48.0 | Mechanical Specification for Microcomputers Using Ruggedized Enhanced Design Implementation (REDI) | 2020 | VITA | |
| 16 | 48.1 | This standard defines the mechanical requirements that are needed to insure the mechanical interchangeability of air cooled 3U and 6U Plug-In Modules and define the features required to achieve Two Level Maintenance compatibility. | 2020 | VITA | |
| 17 | 48.2 | This Standard defines the mechanical requirements that are needed to ensure the mechanical interchangeability of conduction cooled 3U and 6U Plug-In Modules and defines the features required to achieve Two Level Maintenance compatibility. | 2020 | VITA | |
| 18 | 42.3 | XMC PCI Express Protocol Layer Standard - This standard defines the implementation of PCI Express on VITA 42.0, XMC. | 2020 | VITA | |
| 19 | 62.2 | This standard provides requirements for building a 270 volt/3U or 6U class power supply module that can be used to power a VPX chassis in the VITA 62 family of standards in high altitude applications. | 2020 | VITA | 0 |
| 20 | 40 | Service and Status Indicator Standard. This standard defines the colors, behaviors, placement, and labeling of service indicator lamps for boards, field replaceable units, and enclosures. | 2020 | VITA | 0 |
| 21 | 42.3 | XMC PCI Express Protocol Layer Standard - This standard defines the implementation of PCI Express on VITA 42.0, XMC. | 2020 | VITA | 0 |
| 22 | 46.30 | Higher Data Rate VPX | 2020 | VITA | 0 |
| 23 | 48.0 | Mechanical Specification for Microcomputers Using Ruggedized Enhanced Design Implementation (REDI) | 2020 | VITA | 0 |
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|----|------|---|------|------|---|
| 25 | 48.2 | This Standard defines the mechanical requirements that are needed to ensure the mechanical interchangeability of conduction cooled 3U and 6U Plug-In Modules and defines the features required to achieve Two Level Maintenance compatibility. | 2020 | VITA | 0 |
| 26 | 47.0 | Construction, Safety, and Quality for Plug-In Modules Standard | 2019 | VITA | 0 |
| 27 | 47.1 | Common Requirements for Environments, Design and Construction, Safety, and Quality for VITA 47 Plug-In Modules Dot Standard. | 2019 | VITA | 0 |
| 28 | 47.2 | Class 2 Requirements for Environments, Design and Construction, Safety, and Quality for VITA 47 Plug-In Modules Dot Standard | 2019 | VITA | 0 |
| 29 | 47.3 | Class 3 Requirements for Environments, Design and Construction, Safety, and Quality for VITA 47 Plug-In Modules Dot Standard | 2019 | VITA | 0 |
| 30 | 46.0 | VPX Baseline Standard - This standard defines requirements for VPX. | 2019 | VITA | 0 |
| 31 | 46.0 | VPX Baseline Standard - This standard defines requirements for VPX. | 2019 | VITA | 0 |
| 32 | 65.0 | OpenVPX System Standard | 2019 | VITA | 0 |
| 33 | 57.1 | FPGA Mezzanine Card (FMC) Standard - This standard defines the mechanical format and signal assignments for an FPGA mezzanine card interface. | 2019 | VITA | 0 |
| 34 | 65.1 | This standard documents variations of Slot, Backplane, and Modules Profiles. As part of the Slot Profile Description, there are also some Connector Modules defined. This document is primarily tables which are referenced by [VITA 65.0]. | 2019 | VITA | 0 |
| 35 | 67.0 | Coaxial Interconnect on VPX - Base Standard - The objective of this standard is to establish a structure for implementing blind mate analog coaxial interconnects with VPX backplanes and plug-in modules, and to define a specific family of interconnects and | 2019 | VITA | 0 |
| 36 | 67.1 | Coaxial Interconnect on VPX, 3U, 4 Position, SMPM Configuration - The objective of this standard is to detail the configuration and interconnect within the structure of VITA 67.0 enabling a 3U VITA 46 interface containing multi-position blind mate analog | 2019 | VITA | 0 |
| 37 | 86 | High Voltage Input Sealed Connector Power Supply | 2019 | VITA | 0 |
| 38 | 47.0 | Construction, Safety, and Quality for Plug-In Modules Standard | 2019 | VITA | |
| 39 | 47.1 | Common Requirements for Environments, Design and Construction, Safety, and Quality for VITA 47 Plug-In Modules Dot Standard. | 2019 | VITA | |
| 40 | 47.2 | Class 2 Requirements for Environments, Design and Construction, Safety, and Quality for VITA 47 Plug-In Modules Dot Standard | 2019 | VITA | |
| 41 | 47.3 | Class 3 Requirements for Environments, Design and Construction, Safety, and Quality for VITA 47 Plug-In Modules Dot Standard | 2019 | VITA | |
| 42 | 46.0 | VPX Baseline Standard - This standard defines requirements for VPX. | 2019 | VITA | |
| 43 | 65.1 | This standard documents variations of Slot, Backplane, and Modules Profiles. As part of the Slot Profile Description, there are also some Connector Modules defined. This document is primarily tables which are referenced by [VITA 65.0]. | 2019 | VITA | |
| 44 | 57.1 | FPGA Mezzanine Card (FMC) Standard - This standard defines the mechanical format and signal assignments for an FPGA mezzanine card interface. | 2019 | VITA | |
| 45 | 67.0 | Coaxial Interconnect on VPX - Base Standard - The objective of this standard is to establish a structure for implementing blind mate analog coaxial interconnects with VPX backplanes and plug-in modules, and to define a specific family of interconnects and | 2019 | VITA | |
| 46 | 67.1 | Coaxial Interconnect on VPX, 3U, 4 Position, SMPM Configuration - The objective of this standard is to detail the configuration and interconnect within the structure of VITA 67.0 enabling a 3U VITA 46 interface containing multi-position blind mate analog | 2019 | VITA | |
| 47 | 86 | High Voltage Input Sealed Connector Power Supply | 2019 | VITA | |
| 48 | 65.0 | OpenVPX System Standard | 2019 | VITA | |
| 49 | 66.2 | Optical Interconnect On VPX - ARINC 801 Termini Variant â The VITA 66.2 standard defines an ARINC 801 Termini Variant blind mate fiber optic interconnect for use with VPX backplanes and plug-in modules. | 2018 | VITA | |
| 50 | 66.3 | Optical Interconnect On VPX - Mini-Expanded Beam Variant â The VITA 66.3 standard defines an MT Variant blind mate fiber optic interconnect for use with VPX backplanes and plug-in modules. | 2018 | VITA | |

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|----|-----------|---|------|------|---|
| 51 | 57.4 | This standard extends the VITA 57.1 FMC standard by specifying two new connectors that enable additional Gigabit Transceiver interfaces that run at up to 28Gbps. It also describes FMC+ IO modules which support this enhanced version of the FMC electro-mech | 2018 | VITA | |
| 52 | 51.0 | Reliability Prediction - This document provides an electronics failure rate prediction standard, and establishes a Community of Practice. It addresses the limitations of existing prediction practices with a series of subsidiary specifications that contain | 2018 | VITA | |
| 53 | 51.1 | Reliability Prediction MIL-HDBK-217 Subsidiary Specification | 2018 | VITA | |
| 54 | 60.0 | Alternative Connector for VPX - This standard provides an alternate connector to the one specified in the VITA 46.0 VPX Baseline Standard. Because the 46.0 and the 60.0 connectors are not intermateable, a VITA 60.0 module will not plug into a VITA 46.0.0 | 2018 | VITA | |
| 55 | 46.1 | Rear Transition Module for VPX - This standard defines a rear transition module (RTM) for VPX applications. | 2018 | VITA | |
| 56 | 46.3 | Serial RapidIO on VPX Fabric Connector - This standard assigns Serial RapidIO ports to the VPX P1/J1 connector. | 2018 | VITA | |
| 57 | 46.4 | PCI Express on the VPX Fabric Connector - The objective of this standard is the implementation of the PCI Express Fabric in the VITA46 environment and to define the mapping of the PCI Express Links on the VPX connector. | 2018 | VITA | |
| 58 | 46.6 | Gigabit Ethernet Control Plane on VPX - The objectives of this standard are to assign Gigabit Ethernet Port mappings for the purpose of Control Plane communication onto the VPX connectors for both 3U and 6U form factors and to provide rules and recommenda | 2018 | VITA | |
| 59 | 46.7 | Ethernet on VPX Fabric Connector - The objectives of this standard are to assign backplane Ethernet links to the VPX P1/J1 connector and to provide rules and recommendations for the use of Ethernet over backplane media. | 2018 | VITA | |
| 60 | 46.9 | PMC/XMC Rear I/O Fabric Signal Mapping on 3U and 6U VPX Modules Standard - This VITA 46 (VPX) subsidiary standard defines PMC or XMC mezzanine rear I/O pin mappings to VITA 46.0 plug-in module backplane connectors. | 2018 | VITA | |
| 61 | 41.0 | VXS VMEbus Switched Serial Standard - This standard defines a method for using switched serial fabrics within the VMEbus framework. | 2018 | VITA | |
| 62 | 41.1 | VXS 4X InfiniBand Protocol Layer Standard - This standard describes a method for using the InfiniBand protocol on ANSI/VITA 41.0, VXS. | 2018 | VITA | |
| 63 | 41.2 | VXS 4X Serial RapidIO Protocol Layer Standard - This standard describes a method for implementing Serial Rapid I/O on ANSI/VITA 41.0, VXS. | 2018 | VITA | |
| 64 | 42.1 | XMC Switched Mezzanine Card: Parallel RapidIO 8/16 LP-LVDS Protocol Layer Standard - This standard defines the implementation of Parallel RIO on VITA 42.0, XMC. | 2018 | VITA | |
| 65 | 42.2 | XMC Serial RapidIO Protocol Layer Standard - This standard defines the implementation of Serial RIO on VITA 42.0, XMC. | 2018 | VITA | |
| 66 | 48.4 | This standard establishes the mechanical design interface control, outline and mounting requirements for a liquid-flow-through cooled Plug-In Module to ensure the mechanical intermateability of 6U VPX liquid-flow-through cooled Plug-In Module within assoc | 2018 | VITA | |
| 67 | 17.3 | Serial Front Panel Data Port (sFPDP) Gen 3.0 | 2018 | VITA | |
| 68 | 20 | CCPMC - Conduction Cooled PMC - This standard defines the mechanical requirements for compliance with conduction cooled PMC modules. | 2018 | VITA | |
| 69 | 66.2 | Optical Interconnect On VPX - ARINC 801 Termini Variant The VITA 66.2 standard defines an ARINC 801 Termini Variant blind mate fiber optic interconnect for use with VPX backplanes and plug-in modules. | 2018 | VITA | 0 |
| 70 | 66.3 | Optical Interconnect On VPX - Mini-Expanded Beam Variant The VITA 66.3 standard defines an MT Variant blind mate fiber optic interconnect for use with VPX backplanes and plug-in modules. | 2018 | VITA | 0 |
| 71 | 57.4 ERTA | This standard extends the VITA 57.1 FMC standard by specifying two new connectors that enable additional Gigabit Transceiver interfaces that run at up to 28Gbps. It also describes FMC+ IO modules which support this enhanced version of the FMC electro-mech | 2018 | VITA | 0 |
| 72 | 57.4 | This standard extends the VITA 57.1 FMC standard by specifying two new connectors that enable additional Gigabit Transceiver interfaces that run at up to 28Gbps. It also describes FMC+ IO modules which support this enhanced version of the FMC electro-mech | 2018 | VITA | 0 |
| 73 | 42.1 | XMC Switched Mezzanine Card: Parallel RapidIO 8/16 LP-LVDS Protocol Layer Standard - This standard defines the implementation of Parallel RIO on VITA 42.0, XMC. | 2018 | VITA | 0 |

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| 74 | 42.2 | XMC Serial RapidIO Protocol Layer Standard - This standard defines the implementation of Serial RIO on VITA 42.0, XMC. | 2018 | VITA | 0 |
| 75 | 46.1 | Rear Transition Module for VPX - This standard defines a rear transition module (RTM) for VPX applications. | 2018 | VITA | 0 |
| 76 | 46.3 | Serial RapidIO on VPX Fabric Connector - This standard assigns Serial RapidIO ports to the VPX P1/J1 connector. | 2018 | VITA | 0 |
| 77 | 46.4 | PCI Express ¹ on the VPX Fabric Connector - The objective of this standard is the implementation of the PCI Express ¹ Fabric in the VITA46 environment and to define the mapping of the PCI Express ¹ Links on the VPX connector. | 2018 | VITA | 0 |
| 78 | 46.6 | Gigabit Ethernet Control Plane on VPX - The objectives of this standard are to assign Gigabit Ethernet Port mappings for the purpose of Control Plane communication onto the VPX connectors for both 3U and 6U form factors and to provide rules and recommenda | 2018 | VITA | 0 |
| 79 | 46.7 | Ethernet on VPX Fabric Connector - The objectives of this standard are to assign backplane Ethernet links to the VPX P1/J1 connector and to provide rules and recommendations for the use of Ethernet over backplane media. | 2018 | VITA | 0 |
| 80 | 46.9 | PMC/XMC Rear I/O Fabric Signal Mapping on 3U and 6U VPX Modules Standard - This VITA 46 (VPX) subsidiary standard defines PMC or XMC mezzanine rear I/O pin mappings to VITA 46.0 plug-in module backplane connectors. | 2018 | VITA | 0 |
| 81 | 48.4 | This standard establishes the mechanical design interface control, outline and mounting requirements for a liquid-flow-through cooled Plug-In Module to ensure the mechanical intermateability of 6U VPX liquid-flow-through cooled Plug-In Module within assoc | 2018 | VITA | 0 |
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| 83 | 51.1 | Reliability Prediction MIL-HDBK-217 Subsidiary Specification | 2018 | VITA | 0 |
| 84 | 41.0 | VXS VMEbus Switched Serial Standard - This standard defines a method for using switched serial fabrics within the VMEbus framework. | 2018 | VITA | 0 |
| 85 | 41.1 | VXS 4X InfiniBand Protocol Layer Standard - This standard describes a method for using the InfiniBand protocol on ANSI/VITA 41.0, VXS. | 2018 | VITA | 0 |
| 86 | 41.2 | VXS 4X Serial RapidIO Protocol Layer Standard - This standard describes a method for implementing Serial Rapid I/O on ANSI/VITA 41.0, VXS. | 2018 | VITA | 0 |
| 87 | 17.3 | Serial Front Panel Data Port (sFPDP) Gen 3.0 | 2018 | VITA | 0 |
| 88 | 20 | CCPMC - Conduction Cooled PMC - This standard defines the mechanical requirements for compliance with conduction cooled PMC modules. | 2018 | VITA | 0 |
| 89 | 48.8 | Mechanical Standard for Electronic VPX Plug-in Modules Using Air Flow Through Cooling | 2017 | VITA | 0 |
| 90 | 49.02 | VITA Radio Transport (VRT) Standard for Electromagnetic Spectrum: Signals and Applications | 2017 | VITA | 0 |
| 91 | 49.2 | The ANSI/VITA 49.2 standard, which is part of the VITA Radio Transport (VRT) family of standards, defines a signal/spectrum protocol that expresses spectrum observation, spectrum operations, and capabilities of RF devices. This is done independent of manu | 2017 | VITA | 0 |
| 92 | 48.5 | Establishes the design requirements for an air-flow-through cooled plug-in unit with a form factor as close to 6U as possible while retaining the VITA 46 connector layout. Unlike ANSI/VITA 48.1, which uses cooling air impinged directly upon the components | 2017 | VITA | 0 |
| 93 | 53.0 | Standard for Commercial Technology Market Surveillance This standard describes the types of market surveillance data needed by Department of Defense program managers in order to develop and implement technology refresh plans. | 2017 | VITA | 0 |
| 94 | 65.1 | This standard documents variations of Slot, Backplane, and Modules Profiles. As part of the Slot Profile Description, there are also some Connector Modules defined. This document is primarily tables which are referenced by [VITA 65.0]. PDF Version. | 2017 | VITA | 0 |
| 95 | 65.0 | OpenVPX System Standard | 2017 | VITA | 0 |
| 96 | 68.1 | VPX Compliance Channel - Fixed Signal Integrity Budget Standard | 2017 | VITA | 0 |
| 97 | 67.3 | Coaxial Interconnect on VPX, Spring-Loaded Contact on Backplane | 2017 | VITA | 0 |
| 98 | 68.0 | VITA 68.0 is the Base Standard of the VITA 68.x family of standards for signal integrity compliance of VPX systems and components. | 2017 | VITA | 0 |
| 99 | 68.1 ERTA | VPX Compliance Channel - Fixed Signal Integrity Budget Standard | 2017 | VITA | 0 |

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| 100 | 74.0 | Compliant System Small Form Factor Module Base Standard | 2017 | VITA | 0 |
| 101 | 48.5 | Establishes the design requirements for an air-flow-through cooled plug-in unit with a form factor as close to 6U as possible while retaining the VITA 46 connector layout. Unlike ANSI/VITA 48.1, which uses cooling air impinged directly upon the components | 2017 | VITA | |
| 102 | 49.2 | The ANSI/VITA 49.2 standard, which is part of the VITA Radio Transport (VRT) family of standards, defines a signal/spectrum protocol that expresses spectrum observation, spectrum operations, and capabilities of RF devices. This is done independent of manu | 2017 | VITA | |
| 103 | 48.8 | Mechanical Standard for Electronic VPX Plug-in Modules Using Air Flow Through Cooling | 2017 | VITA | |
| 104 | 53.0 | Standard for Commercial Technology Market Surveillance â__ This standard describes the types of market surveillance data needed by Department of Defense program managers in order to develop and implement technology refresh plans. | 2017 | VITA | |
| 105 | 68.0 | VITA 68.0 is the Base Standard of the VITA 68.x family of standards for signal integrity compliance of VPX systems and components. | 2017 | VITA | |
| 106 | 68.1 ERTA | VPX Compliance Channel - Fixed Signal Integrity Budget Standard | 2017 | VITA | |
| 107 | 74.0 | Compliant System Small Form Factor Module Base Standard | 2017 | VITA | |
| 108 | 66.4 | Optical Interconnect On VPX - Half Width MT Variant | 2016 | VITA | |
| 109 | 66.0 | Optical Interconnect on VPX - Base Standard -- The VITA 66.0 base standard defines physical features of a stand-alone compliant blind mate Optical Interconnect for use in VPX systems. | 2016 | VITA | |
| 110 | 62.0 | VPX: Modular Power Supply - This standard provides a set of requirements for power supply modules that can be used in VPX systems. | 2016 | VITA | |
| 111 | 51.2 | Physics of Failure Reliability Predictions - This specification provides standard processes, instructions and default parameters for using the Physics of Failure (PoF) approach for modeling the reliability of electronic products. It includes a discussion | 2016 | VITA | |
| 112 | 51.3 | Qualification and Environmental Stress Screening in Support of Reliability Predictions - This standard provides rules, permissions, and observations to assure that cost effective Qualification and Environmental Stress Screening support valid reliability p | 2016 | VITA | |
| 113 | 41.6 | VXS 1X Gbit Ethernet - This standard describes a method for implementing Ethernet as a control channel on ANSI/VITA 41.0, VXS. | 2016 | VITA | |
| 114 | 42.0 | XMC | 2016 | VITA | |
| 115 | 68.1 | VPX Compliance Channel - Fixed Signal Integrity Budget Standard | 2016 | VITA | 0 |
| 116 | 68.0 | VITA 68.0 is the Base Standard of the VITA 68.x family of standards for signal integrity compliance of VPX systems and components. | 2016 | VITA | 0 |
| 117 | 76.0 | High Performance Cable Standard - Ruggedized 10 Gbaud Bulkhead Connector for Cu and AOC Cables | 2016 | VITA | 0 |
| 118 | 78.00 ERTA | SpaceVPX Systems | 2016 | VITA | 0 |
| 119 | 66.0 | Optical Interconnect on VPX - Base Standard -- The VITA 66.0 base standard defines physical features of a stand-alone compliant blind mate Optical Interconnect for use in VPX systems. | 2016 | VITA | 0 |
| 120 | 66.4 | Optical Interconnect On VPX - Half Width MT Variant | 2016 | VITA | 0 |
| 121 | 67.1 ERTA | Coaxial Interconnect on VPX, 3U, 4 Position, SMPM Configuration - The objective of this standard is to detail the configuration and interconnect within the structure of VITA 67.0 enabling a 3U VITA 46 interface containing multi-position blind mate analog | 2016 | VITA | 0 |
| 122 | 62 | Modular Power Supply Standard | 2016 | VITA | 0 |
| 123 | 60.0 | Alternative Connector for VPX - This standard provides an alternate connector to the one specified in the VITA 46.0 VPX Baseline Standard. Because the 46.0 and the 60.0 connectors are not intermateable, a VITA 60.0 module will not plug into a VITA 46.0.0 | 2016 | VITA | 0 |
| 124 | 51.2 | Physics of Failure Reliability Predictions - This specification provides standard processes, instructions and default parameters for using the Physics of Failure (PoF) approach for modeling the reliability of electronic products. It includes a discussion | 2016 | VITA | 0 |

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| 126 | 41.6 | VXS 1X Gbit Ethernet - This standard describes a method for implementing Ethernet as a control channel on ANSI/VITA 41.0, VXS. | 2016 | VITA | 0 |
| 127 | 42.0 | XMC | 2016 | VITA | 0 |
| 128 | 42.6 | XMC 10 Gigabit Ethernet 4-Lane Protocol Layer Standard - This standard defines a method for supporting 10 Gigabit Ethernet using XAUI switched interconnect protocol on the XMC form factor. | 2015 | VITA | 0 |
| 129 | 49A | Spectrum Survey Interoperability Specification | 2015 | VITA | 0 |
| 130 | 49.0 | The VITA Radio Transport (VRT) standard defines a transport-layer protocol designed to promote interoperability between RF (radio frequency) receivers and signal processing equipment in a wide range of applications. | 2015 | VITA | 0 |
| 131 | 46.10 | Rear Transition Module for VPX | 2015 | VITA | 0 |
| 132 | 46.11 | System Management on VPX | 2015 | VITA | 0 |
| 133 | 17.1 | Serial Front Panel Data Port (sFPDP) - This standard defines Serial FPDP, a high-speed low-latency serial communications protocol for use in high-speed data transfer applications, typically using a fiber optic link. | 2015 | VITA | 0 |
| 134 | 63.0 | Hyperboloid Alternative Connector for VPX | 2015 | VITA | 0 |
| 135 | 49.1 | This standard specifies an optional encapsulation protocol for VITA-49.0 (VRT) packets. | 2015 | VITA | 0 |
| 136 | 78.00 | SpaceVPX Systems | 2015 | VITA | 0 |
| 137 | 17.1 | Serial Front Panel Data Port (sFPDP) - This standard defines a Serial FPDP, a high-speed low-latency serial communications protocol for use in high-speed data transfer applications, typically using a fiber optic link. | 2015 | VITA | |
| 138 | 46.10 | Rear Transition Module for VPX | 2015 | VITA | |
| 139 | 46.11 | System Management on VPX | 2015 | VITA | |
| 140 | 49A | Spectrum Survey Interoperability Specification | 2015 | VITA | |
| 141 | 49.0 | The VITA Radio Transport (VRT) standard defines a transport-layer protocol designed to promote interoperability between RF (radio frequency) receivers and signal processing equipment in a wide range of applications. | 2015 | VITA | |
| 142 | 49.1 | This standard specifies an optional encapsulation protocol for VITA-49.0 (VRT) packets. | 2015 | VITA | |
| 143 | 42.6 | XMC 10 Gigabit Ethernet 4-Lane Protocol Layer Standard - This standard defines a method for supporting 10 Gigabit Ethernet using XAUI switched interconnect protocol on the XMC form factor. | 2015 | VITA | |
| 144 | 63.0 | Hyperboloid Alternative Connector for VPX | 2015 | VITA | |
| 145 | 78.00 | SpaceVPX Systems | 2015 | VITA | |
| 146 | 61.0 | XMC 2.0 - This standard, based upon VITA 42.0 XMC, defines an open standard for supporting high-speed, switched interconnect protocols on an existing, widely deployed form factor, but utilizing an alternate, ruggedized, high speed mezzanine interconnector | 2014 | VITA | |
| 147 | 58.0 | This standard provides common design and performance requirements for a family of integrated electronic chassis incorporating updated industry standard high speed electronic assemblies and designed for rugged environments. | 2014 | VITA | |
| 148 | 39 | PCI-X for PMC and Processor PMC - This standard integrates the PCI-X capability from PCI to PMC based products, including standard PMCs as well as Processor PMCs. | 2014 | VITA | |
| 149 | 48.7 | Mechanical Standard for Electronic Plug-in units using Air Flow-By Cooling Technology | 2014 | VITA | |
| 150 | 30.1 | 2mm Connector Practice for Conduction Cooled Euroboard Systems - This standard defines the dimensions for conduction cooled Euroboards when used with 2mm connectors. | 2014 | VITA | |
| 151 | 31.1 | Gigabit Ethernet on VME64x Backplanes - This standard defines a pin assignment and interconnection methodology for implementing a 10/100/1000BASE-T Ethernet switched network on a ANSI/VITA 1.1 VME64x backplane. | 2014 | VITA | |
| 152 | 32 | Processor PMC - This standard incorporates a set of extensions to the IEEE 1386.1 PMC (PCI Mezzanine Card) standard which creates a new class of CPU based PMC cards referred to in this standard as Processor PMC cards. | 2014 | VITA | |

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| 153 | 1.7 | Increased Current DIN Connector- This standard describes increased current levels, test methods, test data and compliance criteria for 3 row DIN and 5 row DIN connectors when used in VME, VME64 and VME64 Extension P1/J1 and P2/J2 pin out arrangements. | 2014 | VITA | |
| 154 | 1.5 | 2eSST - This standard defines a new VME protocol that allows data transfers of up to 320 Mbytes/second | 2014 | VITA | |
| 155 | 61.0 | XMC 2.0 - This standard, based upon VITA 42.0 XMC, defines an open standard for supporting high-speed, switched interconnect protocols on an existing, widely deployed form factor, but utilizing an alternate, ruggedized, high speed mezzanine interconnector | 2014 | VITA | 0 |
| 156 | 58.0 | This standard provides common design and performance requirements for a family of integrated electronic chassis incorporating updated industry standard high speed electronic assemblies and designed for rugged environments. | 2014 | VITA | 0 |
| 157 | 30.1 | 2mm Connector Practice for Conduction Cooled Euroboard Systems - This standard defines the dimensions for conduction cooled Euroboards when used with 2mm connectors. | 2014 | VITA | 0 |
| 158 | 31.1 | Gigabit Ethernet on VME64x Backplanes - This standard defines a pin assignment and interconnection methodology for implementing a 10/100/1000BASE-T Ethernet switched network on a ANSI/VITA 1.1 VME64x backplane. | 2014 | VITA | 0 |
| 159 | 32 | Processor PMC - This standard incorporates a set of extensions to the IEEE 1386.1 PMC (__ PCI Mezzanine Card __) standard which creates a new class of CPU based PMC cards referred to in this standard as Processor PMC cards. | 2014 | VITA | 0 |
| 160 | 39 | PCI-X for PMC and Processor PMC - This standard integrates the PCI-X capability from PCI to PMC based products, including standard PMCs as well as Processor PMCs. | 2014 | VITA | 0 |
| 161 | 1.5 | 2eSST - This standard defines a new VME protocol that allows data transfers of up to 320 Mbytes/second | 2014 | VITA | 0 |
| 162 | 1.7 | Increased Current DIN Connector- This standard describes increased current levels, test methods, test data and compliance criteria for 3 row DIN and 5 row DIN connectors when used in VME, VME64 and VME64 Extension P1/J1 and P2/J2 pin out arrangements. | 2014 | VITA | 0 |
| 163 | 48.7 | Mechanical Standard for Electronic Plug-in units using Air Flow-By Cooling Technology | 2014 | VITA | 0 |
| 164 | 42.0 | XMC | 2014 | VITA | 0 |
| 165 | 42.3 | XMC PCI Express Protocol Layer Standard - This standard defines the implementation of PCI Express on VITA 42.0, XMC. | 2014 | VITA | 0 |
| 166 | 46.0 | VPX Baseline Standard - This standard defines requirements for VPX. | 2013 | VITA | 0 |
| 167 | 46.1 | Rear Transition Module for VPX - This standard defines a rear transition module (RTM) for VPX applications. | 2013 | VITA | 0 |
| 168 | 46.9 ERTA | PMC/XMC Rear I/O Fabric Signal Mapping on 3U and 6U VPX Modules Standard - This VITA 46 (VPX) subsidiary standard defines PMC or XMC mezzanine rear I/O pin mappings to VITA 46.0 plug-in module backplane connectors. | 2013 | VITA | 0 |
| 169 | 46.6 | Gigabit Ethernet Control Plane on VPX - The objectives of this standard are to assign Gigabit Ethernet Port mappings for the purpose of Control Plane communication onto the VPX connectors for both 3U and 6U form factors and to provide rules and recommenda | 2013 | VITA | 0 |
| 170 | 46.11 | System Management on VPX | 2013 | VITA | 0 |
| 171 | 51.1 | Reliability Prediction MIL-HDBK-217 Subsidiary Specification | 2013 | VITA | 0 |
| 172 | 38 | Describes a methodology for using IPMI for System Management of VME systems. | 2013 | VITA | 0 |
| 173 | 58.1 | Line Replaceable Integrated Electronics Chassis Standard, Liquid Cooled Chassis - The objective of this standard is to identify the particular requirements for a chassis configuration conforming to the ANSI/VITA 58.0 base standard. | 2013 | VITA | 0 |
| 174 | 66.2 | Optical Interconnect On VPX - ARINC 801 Termini Variant The VITA 66.2 standard defines an ARINC 801 Termini Variant blind mate fiber optic interconnect for use with VPX backplanes and plug-in modules. | 2013 | VITA | 0 |
| 175 | 73.0 | VITA 73.0 Rugged Small Form Factor - This document provides mechanical and electrical guidelines for the standardization of switched serial interconnects in small form-factor applications, with specific concern taken to allow deployment in ruggedized envi | 2013 | VITA | 0 |
| 176 | 74.0 | Compliant System Small Form Factor Module Base Standard | 2013 | VITA | 0 |
| 177 | 38 | Describes a methodology for using IPMI for System Management of VME systems. | 2013 | VITA | |
| 178 | 58.1 | Line Replaceable Integrated Electronics Chassis Standard, Liquid Cooled Chassis - The objective of this standard is to identify the particular requirements for a chassis configuration conforming to the ANSI/VITA 58.0 base standard. | 2013 | VITA | |

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|-----|-------|--|------|------|---|
| 179 | 73.0 | VITA 73.0 Rugged Small Form Factor - This document provides mechanical and electrical guidelines for the standardization of switched serial interconnects in small form-factor applications, with specific concern taken to allow deployment in ruggedized envi | 2013 | VITA | |
| 180 | 75.0 | VITA 75 Rugged Small Form Factor - This draft standard for a rugged small form factor describes overall subsystem attributes such as the envelope of the subsystem box and the organization of the dot specifications. | 2012 | VITA | |
| 181 | 75.11 | This draft standard provides requirements for front panels, connectors, signal pin assignments, and power for VITA 75 subsystems. | 2012 | VITA | |
| 182 | 75.20 | Rugged Small Form Factor ù Cooled via Free Air Convection | 2012 | VITA | |
| 183 | 75.22 | This draft standard standardizes mounting and cooling for conduction to a cold plate cooled VITA 75 subsystems. | 2012 | VITA | |
| 184 | 66.1 | Optical Interconnect On VPX - MT Variant â The VITA 66.1 standard defines an MT Variant blind mate fiber optic interconnect for use with VPX backplanes and plug-in modules. | 2012 | VITA | |
| 185 | 10 | SKYchannel - This standard was withdrawn as an American National Standard in 2012 and is provided for historical reference only. This standard defines a packet switched cross bar interconnect that runs on the VMEbus P2 connector. | 2012 | VITA | |
| 186 | 75.0 | VITA 75 Rugged Small Form Factor - This draft standard for a rugged small form factor describes overall subsystem attributes such as the envelope of the subsystem box and the organization of the dot specifications. | 2012 | VITA | 0 |
| 187 | 75.11 | This draft standard provides requirements for front panels, connectors, signal pin assignments, and power for VITA 75 subsystems. | 2012 | VITA | 0 |
| 188 | 75.20 | Rugged Small Form Factor ù Cooled via Free Air Convection | 2012 | VITA | 0 |
| 189 | 75.22 | This draft standard standardizes mounting and cooling for conduction to a cold plate cooled VITA 75 subsystems. | 2012 | VITA | 0 |
| 190 | 12 | M-Module - This standard defines a mezzanine module specification for small sized printed circuit boards. | 2012 | VITA | |
| 191 | 65 | The OpenVPX System Specification was created to bring versatile system architectural solutions to the VPX market. Based on the extremely flexible VPX family of standards, the OpenVPX standard uses module mechanical, connectors, thermal, communications pro | 2012 | VITA | 0 |
| 192 | 67.1 | Coaxial Interconnect on VPX, 3U, 4 Position, SMPM Configuration - The objective of this standard is to detail the configuration and interconnect within the structure of VITA 67.0 enabling a 3U VITA 46 interface containing multi-position blind mate analog | 2012 | VITA | 0 |
| 193 | 67.2 | Coaxial Interconnect on VPX, 8 Position SMPM Configuration - The objective of this standard is to detail the configuration and interconnect within the structure of VITA 67.0 enabling a VITA 46 interface containing multi-position blind mate analog connecto | 2012 | VITA | 0 |
| 194 | 67.0 | Coaxial Interconnect on VPX - Base Standard - The objective of this standard is to establish a structure for implementing blind mate analog coaxial interconnects with VPX backplanes and plug-in modules, and to define a specific family of interconnects and | 2012 | VITA | 0 |
| 195 | 66.3 | Optical Interconnect On VPX - Mini-Expanded Beam Variant The VITA 66.3 standard defines an MT Variant blind mate fiber optic interconnect for use with VPX backplanes and plug-in modules. | 2012 | VITA | 0 |
| 196 | 60.0 | Alternative Connector for VPX - This standard provides an alternate connector to the one specified in the VITA 46.0 VPX Baseline Standard. Because the 46.0 and the 60.0 connectors are not intermateable, a VITA 60.0 module will not plug into a VITA 46.0.0 | 2012 | VITA | 0 |
| 197 | 62.0 | VPX: Modular Power Supply - This standard provides a set of requirements for power supply modules that can be used in VPX systems. | 2012 | VITA | 0 |
| 198 | 10 | SKYchannel - This standard was withdrawn as an American National Standard in 2012 and is provided for historical reference only. This standard defines a packet switched cross bar interconnect that runs on the VMEbus P2 connector. | 2012 | VITA | 0 |
| 199 | 12 | M-Module - This standard defines a mezzanine module specification for small sized printed circuit boards. | 2012 | VITA | 0 |
| 200 | 51.0 | Reliability Prediction - This document provides an electronics failure rate prediction standard, and establishes a Community of Practice. It addresses the limitations of existing prediction practices with a series of subsidiary specifications that contain | 2012 | VITA | 0 |
| 201 | 46.3 | Serial RapidIO on VPX Fabric Connector - This standard assigns Serial RapidIO ports to the VPX P1/J1 connector. | 2012 | VITA | 0 |
| 202 | 46.4 | PCI Express ¹ on the VPX Fabric Connector - The objective of this standard is the implementation of the PCI Express ¹ Fabric in the VITA46 environment and to define the mapping of the PCI Express ¹ Links on the VPX connector. | 2012 | VITA | 0 |

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| 203 | 46.7 | Ethernet on VPX Fabric Connector - The objectives of this standard are to assign backplane Ethernet links to the VPX P1/J1 connector and to provide rules and recommendations for the use of Ethernet over backplane media. | 2012 | VITA | 0 |
| 204 | 42.2 | XMC Serial RapidIO Protocol Layer Standard - This standard defines the implementation of Serial RIO on VITA 42.0, XMC. | 2012 | VITA | 0 |
| 205 | 42.1 | XMC Switched Mezzanine Card: Parallel RapidIO 8/16 LP-LVDS Protocol Layer Standard - This standard defines the implementation of Parallel RIO on VITA 42.0, XMC. | 2012 | VITA | 0 |
| 206 | 46.8 | InfiniBand on VPX Fabric Connector - The objectives of this draft standard are to assign InfiniBand ports to the VPX connectors and to provide rules and recommendations for the use of the assigned InfiniBand ports. | 2011 | VITA | 0 |
| 207 | 51.2 | Physics of Failure Reliability Predictions - This specification provides standard processes, instructions and default parameters for using the Physics of Failure (PoF) approach for modeling the reliability of electronic products. It includes a discussion | 2011 | VITA | 0 |
| 208 | 6 | SCSA - This standard defines an isochronous backplane bus for telephony applications on the VMEbus P2 connector. | 2011 | VITA | 0 |
| 209 | 4.1 | IP I/O Mapping to VME64x - This standard defines the pin assignments from IP Modules to the VME64x P0 and P2 connectors. | 2011 | VITA | 0 |
| 210 | 4 | IP Module - This standard defines the requirements for a business card sized mezzanine module printed circuit board. | 2011 | VITA | 0 |
| 211 | 5.1 | RACEway Interlink - This standard defines a high speed circuit switched point to point interconnect for use between VMEbus modules via the P2 connector. | 2011 | VITA | 0 |
| 212 | 6.1 | SCSA Extensions - This standard provides feature extensions to the ANSI/VITA 6 standard. | 2011 | VITA | 0 |
| 213 | 1.6 | Keying for Conduction Cooled VME64x. | 2011 | VITA | 0 |
| 214 | 1 | VME64 Standard - This standard covers the main body of the VMEbus specification. It includes both 32 bit and 64 bit usage. | 2011 | VITA | 0 |
| 215 | 3 | Board Level Live Insertion - This standard defines several methodologies for using VMEbus modules in a live insertion framework. | 2011 | VITA | 0 |
| 216 | 1.1 | VME64 Extensions - This standard covers extensions to the VME64 specification including the 160 pin connector, geographical addressing, and added power pins. | 2011 | VITA | 0 |
| 217 | 1.3 | VME64x 9U x 400 mm Format - This standard defines a 9U x 400 mm board layout for use within the VMEbus framework. | 2011 | VITA | 0 |
| 218 | 35 | Provides pin assignments for PMC P4 connector to VME P0 and P2 connectors. | 2011 | VITA | 0 |
| 219 | 30 | 2mm Connector Practice for Euroboard Systems - This standards provides the dimensions for Euroboard systems that use 2mm connectors. | 2011 | VITA | 0 |
| 220 | 41.2 | VXS 4X Serial RapidIO Protocol Layer Standard - This standard describes a method for implementing Serial Rapid I/O on ANSI/VITA 41.0, VXS. | 2011 | VITA | 0 |
| 221 | 20 | CCPMC - Conduction Cooled PMC - This standard defines the mechanical requirements for compliance with conduction cooled PMC modules. | 2011 | VITA | 0 |
| 222 | 17 | Front Panel Data Port (FPDP) - This standard defines a point to point data interconnect for use on front panel Eurocard modules. | 2011 | VITA | 0 |
| 223 | 23 | VME64 Extensions for Physics - This standard defines a series of recommended practices for the use of VMEbus in the physics community. | 2011 | VITA | 0 |
| 224 | 26 | Myrinet - This standard defines a packet switched interconnect protocol for implementation in a VMEbus environment. | 2011 | VITA | 0 |
| 225 | 66.0 | Optical Interconnect on VPX - Base Standard -- The VITA 66.0 base standard defines physical features of a stand-alone compliant blind mate Optical Interconnect for use in VPX systems. | 2011 | VITA | 0 |
| 226 | 66.1 | Optical Interconnect On VPX - MT Variant The VITA 66.1 standard defines an MT Variant blind mate fiber optic interconnect for use with VPX backplanes and plug-in modules. | 2011 | VITA | 0 |
| 227 | 17 | Front Panel Data Port (FPDP) - This standard defines a point to point data interconnect for use on front panel Eurocard modules. | 2011 | VITA | |
| 228 | 1.6 | Keying for Conduction Cooled VME64x. | 2011 | VITA | |

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|-----|-------|--|------|------|---|
| 229 | 3 | Board Level Live Insertion - This standard defines several methodologies for using VMEbus modules in a live insertion framework. | 2011 | VITA | |
| 230 | 4 | IP Module - This standard defines the requirements for a business card sized mezzanine module printed circuit board. | 2011 | VITA | |
| 231 | 4.1 | IP I/O Mapping to VME64x - This standard defines the pin assignments from IP Modules to the VME64x P0 and P2 connectors. | 2011 | VITA | |
| 232 | 5.1 | RACEway Interlink - This standard defines a high speed circuit switched point to point interconnect for use between VMEbus modules via the P2 connector. | 2011 | VITA | |
| 233 | 6 | SCSA - This standard defines an isochronous backplane bus for telephony applications on the VMEbus P2 connector. | 2011 | VITA | |
| 234 | 6.1 | SCSA Extensions - This standard provides feature extensions to the ANSI/VITA 6 standard. | 2011 | VITA | |
| 235 | 35 | Provides pin assignments for PMC P4 connector to VME P0 and P2 connectors. | 2011 | VITA | |
| 236 | 23 | VME64 Extensions for Physics - This standard defines a series of recommended practices for the use of VMEbus in the physics community. | 2011 | VITA | |
| 237 | 26 | Myrinet - This standard defines a packet switched interconnect protocol for implementation in a VMEbus environment. | 2011 | VITA | |
| 238 | 30 | 2mm Connector Practice for Euroboard Systems - This standards provides the dimensions for Euroboard systems that use 2mm connectors. | 2011 | VITA | |
| 239 | 1 | VME64 Standard - This standard covers the main body of the VMEbus specification. It includes both 32 bit and 64 bit usage. | 2011 | VITA | |
| 240 | 1.1 | VME64 Extensions - This standard covers extensions to the VME64 specification including the 160 pin connector, geographical addressing, and added power pins. | 2011 | VITA | |
| 241 | 1.3 | VME64x 9U x 400 mm Format - This standard defines a 9U x 400 mm board layout for use within the VMEbus framework | 2011 | VITA | |
| 242 | 46.8 | InfiniBand on VPX Fabric Connector - The objectives of this draft standard are to assign InfiniBand ports to the VPX connectors and to provide rules and recommendations for the use of the assigned InfiniBand ports. | 2011 | VITA | |
| 243 | 65 | The OpenVPX System Specification was created to bring versatile system architectural solutions to the VPX market. Based on the extremely flexible VPX family of standards, the OpenVPX standard uses module mechanical, connectors, thermal, communications pro | 2010 | VITA | 0 |
| 244 | 57.1 | FPGA Mezzanine Card (FMC) Standard - This standard defines the mechanical format and signal assignments for an FPGA mezzanine card interface. | 2010 | VITA | 0 |
| 245 | 53.0 | Standard for Commercial Technology Market Surveillance This standard describes the types of market surveillance data needed by Department of Defense program managers in order to develop and implement technology refresh plans. | 2010 | VITA | 0 |
| 246 | 51.3 | Qualification and Environmental Stress Screening in Support of Reliability Predictions - This standard provides rules, permissions, and observations to assure that cost effective Qualification and Environmental Stress Screening support valid reliability p | 2010 | VITA | 0 |
| 247 | 48.0 | Mechanical Specification for Microcomputers Using Ruggedized Enhanced Design Implementation (REDI) | 2010 | VITA | 0 |
| 248 | 48.5 | Establishes the design requirements for an air-flow-through cooled plug-in unit with a form factor as close to 6U as possible while retaining the VITA 46 connector layout. Unlike ANSI/VITA 48.1, which uses cooling air impinged directly upon the components | 2010 | VITA | 0 |
| 249 | 48.2 | This Standard defines the mechanical requirements that are needed to ensure the mechanical interchangeability of conduction cooled 3U and 6U Plug-In Modules and defines the features required to achieve Two Level Maintenance compatibility. | 2010 | VITA | 0 |
| 250 | 48.1 | This standard defines the mechanical requirements that are needed to insure the mechanical interchangeability of air cooled 3U and 6U Plug-In Modules and define the features required to achieve Two Level Maintenance compatibility. | 2010 | VITA | 0 |
| 251 | 46.9 | PMC/XMC Rear I/O Fabric Signal Mapping on 3U and 6U VPX Modules Standard - This VITA 46 (VPX) subsidiary standard defines PMC or XMC mezzanine rear I/O pin mappings to VITA 46.0 plug-in module backplane connectors. | 2010 | VITA | 0 |
| 252 | 46.4 | PCI Express ¹ on the VPX Fabric Connector - The objective of this standard is the implementation of the PCI Express ¹ Fabric in the VITA46 environment and to define the mapping of the PCI Express ¹ Links on the VPX connector. | 2010 | VITA | 0 |
| 253 | 46.10 | Rear Transition Module for VPX | 2009 | VITA | 0 |
| 254 | 42.6 | XMC 10 Gigabit Ethernet 4-Lane Protocol Layer Standard - This standard defines a method for supporting 10 Gigabit Ethernet using XAUI switched interconnect protocol on the XMC form factor. | 2009 | VITA | 0 |

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|-----|------|--|------|------|---|
| 255 | 49.1 | This standard specifies an optional encapsulation protocol for VITA-49.0 (VRT) packets. | 2009 | VITA | 0 |
| 256 | 49.1 | This standard specifies an optional encapsulation protocol for VITA-49.0 (VRT) packets. | 2009 | VITA | 0 |
| 257 | 49.0 | The VITA Radio Transport (VRT) standard defines a transport-layer protocol designed to promote interoperability between RF (radio frequency) receivers and signal processing equipment in a wide range of applications. | 2009 | VITA | 0 |
| 258 | 49.0 | The VITA Radio Transport (VRT) standard defines a transport-layer protocol designed to promote interoperability between RF (radio frequency) receivers and signal processing equipment in a wide range of applications. | 2009 | VITA | 0 |
| 259 | 41.6 | VXS 1X Gbit Ethernet - This standard describes a method for implementing Ethernet as a control channel on ANSI/VITA 41.0, VXS. | 2009 | VITA | 0 |
| 260 | 41.6 | VXS 1X Gigabit Ethernet Control Channel Layer Standard | 2009 | VITA | 0 |
| 261 | 31.1 | Gigabit Ethernet on VME64x Backplanes - This standard defines a pin assignment and interconnection methodology for implementing a 10/100/1000BASE-T Ethernet switched network on a ANSI/VITA 1.1 VME64x backplane. | 2009 | VITA | 0 |
| 262 | 17.1 | Serial Front Panel Data Port (sFPDP) - This standard defines Serial FPDP, a high-speed low-latency serial communications protocol for use in high-speed data transfer applications, typically using a fiber optic link. | 2009 | VITA | 0 |
| 263 | 58.0 | This standard provides common design and performance requirements for a family of integrated electronic chassis incorporating updated industry standard high speed electronic assemblies and designed for rugged environments. | 2009 | VITA | 0 |
| 264 | 57.1 | FPGA Mezzanine Card (FMC) Standard - This standard defines the mechanical format and signal assignments for an FPGA mezzanine card interface. | 2008 | VITA | 0 |
| 265 | 30.1 | 2mm Connector Practice for Conduction Cooled Euroboard Systems - This standard defines the dimensions for conduction cooled Euroboards when used with 2mm connectors. | 2008 | VITA | 0 |
| 266 | 42.0 | XMC | 2008 | VITA | 0 |
| 267 | 46.7 | Ethernet on VPX Fabric Connector - The objectives of this standard are to assign backplane Ethernet links to the VPX P1/J1 connector and to provide rules and recommendations for the use of Ethernet over backplane media. | 2008 | VITA | 0 |
| 268 | 46.0 | VPX Baseline Standard - This standard defines requirements for VPX. | 2007 | VITA | 0 |
| 269 | 46.0 | VPX Baseline Standard - This standard defines requirements for VPX. | 2007 | VITA | 0 |
| 270 | 47 | This standard defines environmental, design and construction, safety, and quality requirements for commercial-off-the-shelf (COTS) plug-in units (cards, modules, etc.) intended for mobile applications. | 2007 | VITA | 0 |
| 271 | 41.1 | VXS 4X InfiniBand Protocol Layer Standard - This standard describes a method for using the InfiniBand protocol on ANSI/VITA 41.0, VXS. | 2006 | VITA | 0 |
| 272 | 41.0 | VXS VMEbus Switched Serial Standard - This standard defines a method for using switched serial fabrics within the VMEbus framework. | 2006 | VITA | 0 |
| 273 | 13 | VMEbus Pin Assignment Standard for ISO/IEC 14575 (IEEE Std. 1355-1995 (H.I.C.)) - Historical Standard. This standard was withdrawn in 2006 and is provided for historical reference only. | 2006 | VITA | |
| 274 | 19.1 | BusNet Media Access Control - Historical Standard. This standard was withdrawn in 2006 and is provided for historical reference only. This standard defines the media access control layer for the BusNet backplane software protocol. | 2006 | VITA | |
| 275 | 19.2 | BusNet Link Layer Control - Historical Standard. This standard was withdrawn in 2006 and is provided for historical reference only. This standard defines the link layer control layer for the Busnet backplane software protocol. | 2006 | VITA | |
| 276 | 25 | VISION - Historical Standard. This standard was withdrawn in 2006 and is provided for historical reference only. This standard defines a software application interface for VMEbus modules. | 2006 | VITA | |
| 277 | 29 | PC.MIP - Historical Standard. This standard was withdrawn in 2006 and is provided for historical reference only. This standard defines the mechanical form factor and the pin assignments for a small form factor mezzanine module based on the PCI bus. | 2006 | VITA | |
| 278 | 40 | Service and Status Indicator Standard. This standard defines the colors, behaviors, placement, and labeling of service indicator lamps for boards, field replaceable units, and enclosures. | 2003 | VITA | 0 |
| 279 | 17.1 | Serial Front Panel Data Port (sFPDP) - This standard defines Serial FPDP, a high-speed low-latency serial communications protocol for use in high-speed data transfer applications, typically using a fiber optic link. | 2003 | VITA | 0 |
| 280 | 1.3 | VME64x 9U x 400 mm Format - This standard defines a 9U x 400 mm board layout for use within the VMEbus framework. | 2003 | VITA | 0 |

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|-----|------|--|------|------|---|
| 281 | 1.1 | VME64 Extensions - This standard covers extensions to the VME64 specification including the 160 pin connector, geographical addressing, and added power pins. | 2003 | VITA | 0 |
| 282 | 40 | Service and Status Indicator Standard. This standard defines the colors, behaviors, placement, and labeling of service indicator lamps for boards, field replaceable units, and enclosures. | 2002 | VITA | 0 |
| 283 | 29 | PC.MIP - Historical Standard. This standard was withdrawn in 2006 and is provided for historical reference only. This standard defines the mechanical form factor and the pin assignments for a small form factor mezzanine module based on the PCI bus. | 2001 | VITA | 0 |
| 284 | 1.5 | 2eSST - This standard defines a new VME protocol that allows data transfers of up to 320 Mbytes/second. Reaffirmed in 2009. Stabilized in 2014. | 1999 | VITA | 0 |
| 285 | 5.1 | RACEway Interlink - This standard defines a high speed circuit switched point to point interconnect for use between VMEbus modules via the P2 connector. | 1999 | VITA | 0 |
| 286 | 1.4 | VME64x Live Insertion System Requirements | 1998 | VITA | 0 |
| 287 | 26 | Myrinet - This standard defines a packet switched interconnect protocol for implementation in a VMEbus environment. | 1998 | VITA | 0 |
| 288 | 23 | VME64 Extensions for Physics - This standard defines a series of recommended practices for the use of VMEbus in the physics community. | 1998 | VITA | 0 |
| 289 | 17 | Front Panel Data Port (FPDP) - This standard defines a point to point data interconnect for use on front panel Eurocard modules. | 1998 | VITA | 0 |
| 290 | 19.1 | BusNet Media Access Control - Historical Standard. This standard was withdrawn in 2006 and is provided for historical reference only. This standard defines the media access control layer for the BusNet backplane software protocol. | 1998 | VITA | 0 |
| 291 | 19.2 | BusNet Link Layer Control - Historical Standard. This standard was withdrawn in 2006 and is provided for historical reference only. This standard defines the link layer control layer for the Busnet backplane software protocol. | 1998 | VITA | 0 |
| 292 | 19.0 | Summary and Introduction to the BusNet Standard | 1997 | VITA | 0 |
| 293 | 25 | VISION - Historical Standard. This standard was withdrawn in 2006 and is provided for historical reference only. This standard defines a software application interface for VMEbus modules. | 1997 | VITA | 0 |
| 294 | 1.1 | VME64 Extensions - This standard covers extensions to the VME64 specification including the 160 pin connector, geographical addressing, and added power pins. | 1997 | VITA | 0 |
| 295 | 1.3 | VME64x 9U x 400 mm Format - This standard defines a 9U x 400 mm board layout for use within the VMEbus framework | 1997 | VITA | 0 |
| 296 | 4.1 | IP I/O Mapping to VME64x - This standard defines the pin assignments from IP Modules to the VME64x P0 and P2 connectors. | 1996 | VITA | 0 |
| 297 | 6.1 | SCSA Extensions - This standard provides feature extensions to the ANSI/VITA 6 standard. | 1996 | VITA | 0 |
| 298 | 12 | M-Module - This standard defines a mezzanine module specification for small sized printed circuit boards. | 1996 | VITA | 0 |
| 299 | 10 | SKYchannel - This standard was withdrawn as an American National Standard in 2012 and is provided for historical reference only. This standard defines a packet switched cross bar interconnect that runs on the VMEbus P2 connector. | 1995 | VITA | 0 |
| 300 | 13 | VMEbus Pin Assignment Standard for ISO/IEC 14575 (IEEE Std. 1355-1995 (H.I.C.)) - Historical Standard. This standard was withdrawn in 2006 and is provided for historical reference only. | 1995 | VITA | 0 |
| 301 | 4 | IP Module - This standard defines the requirements for a business card sized mezzanine module printed circuit board. | 1995 | VITA | 0 |
| 302 | 3 | Board Level Live Insertion - This standard defines several methodologies for using VMEbus modules in a live insertion framework. | 1995 | VITA | 0 |
| 303 | 1 | VME64 Standard - This standard covers the main body of the VMEbus specification. It includes both 32 bit and 64 bit usage. | 1994 | VITA | 0 |
| 304 | 6 | SCSA - This standard defines an isochronous backplane bus for telephony applications on the VMEbus P2 connector. | 1994 | VITA | 0 |

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